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Name     CMEADDR ;
PartNo   00 ;
Date     4/9/2000 ;
Revision 08.0;
Designer Dan Kohn ;
Company   ;
Assembly None ;
Location  ;
Device   p22v10 ;

/* *********************** INPUT PINS *************************/
PIN   2  = RW                          ; /* R/!W from Bus_Port    */
PIN   3  = A0                          ; /* A0 from Bus_Port      */
PIN   4  = A1                          ; /* A1 From Bus_Port      */
PIN   5  = A2                          ; /* A2 from Bus_Port      */
PIN   6  = A3                          ; /* A3 from Bus_Port      */
PIN   7  = CSn                         ; /* CSn from Bus_Port     */

/* ********************** OUTPUT PINS *************************/
PIN   23  = IO0                        ; /*   See Descriptions    */
PIN   22  = IO1                        ; /*        Below          */
PIN   21  = IO2                        ;
PIN   20  = IO3                        ;
PIN   19  = IO4                        ;
PIN   18  = IO5                        ;
PIN   17  = IO6                        ;
PIN   16  = IO7                        ;
pin   15  = t6                         ;
pin   14  = t7                         ;
/************ INPUT AND OUTPUT PORT ADDRESSING LOGIC ************/

IO0 = !(!CSn & RW & !A3 & !A2 & !A1 & !A0)    ; /* Input Port 1 */

IO1 = !(!CSn & RW & !A3 & !A2 & !A1 &  A0)    ; /* Input Port 2 */

IO2 = !(!CSn & !RW & !A3 & !A2 & A1 & !A0)    ; /* Output Port  */

IO3 = !(!CSn & !RW & !A3 & !A2 & A1 & A0)     ; /* Motor Port   */


/***************** DIGITAL to ANALOG CONVERTER LOGIC ************/
/* WHERE IO4 -> !LDCA of TLC7225                                */
/*       IO5 -> !WR of TLC7225                                  */
/*       IO6 -> A0 of TLC7225                                   */
/*       IO7 -> A1 of TLC7225                                   */

IO4 = !(!CSn & !RW & A3 & !A2 & !A1 & !A0);

IO5 = !(!CSn & !RW & !A3 & A2);

t6 = (!CSn & !RW & !A3 & A2 & !A1 & A0) # (!CSn & !RW & !A3 & A2 & A1 & A0);

t7 = (!CSn & !RW & !A3 & A2 & A1 & !A0) # (!CSn & !RW & !A3 & A2 & A1 & A0);

IO6 = t6;

IO7 = t7;
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